

CENTRE FOR DEVELOPMENT OF TELEMATICS

C-DOT/P&HR/Rectt./11/2023

22.11.2023

**ADVERTISEMENT FOR ENGAGEMENT OF DYNAMIC YOUNG
MANPOWER FOR SHORT TERM CONTRACT AT C-DOT DELHI**

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| Job Role | A. Project Coordinator - Cyber Security - Code - T0101 |
| No. of Vacancy | 01 no. |
| Location | C-DOT Delhi |
| Qualification | Bachelor's degree (Candidate having graduation/Post graduation in Engineering/Science/Technology or MBA or doctorate in the relevant field would be given added advantage) |
| CTC | 1.5 lakhs per month (18 lakhs per annum) |
| Experience | <ul style="list-style-type: none">• Minimum 5-10 years of full-time experience (preferably in Defence sector, CAPF would be given advantage) |
| Roles & Responsibilities | <ul style="list-style-type: none">• Coordinating cyber security project in conformance with timeline and deliverables requirement• Facilitating communication with the Internal and External stakeholders• To provide inputs in coordinating with many components related to the development of an entire cyber security ecosystem• Provide inputs on creating strategic partnerships in the area of cyber security research and innovation, and onboarding different stakeholders for promoting developed solutions• Liaising with clients to identify and define project requirements, scope and objectives Act as the point of contact and communicate project status to all participants• Create and maintain comprehensive project proposal, documentation, budgeting, plans and |

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| | reports |
| Skills | <ul style="list-style-type: none"> • Experience of dealing with large no. of users/customers through different channels • Excellent communication, leadership, and interpersonal skills • Proven work experience in handling cyber security projects. Good understanding of cyber security work areas is desired • Experience in project management, from conception to delivery • Solid organizational skills, including multitasking and time-management • Exceptional verbal, written, and presentation skills • Various certifications including Security+, CEH, GIAC, CASP, and CISSP is an added advantage |

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| Job Role | B. Cyber Security Data Analyst- Code - T0102 |
| No. of Vacancy | 01 no. |
| Location | C-DOT Delhi |
| Qualification | Bachelor's degree. (Candidate having graduation/Post graduation in Engineering/Science/Technology in the relevant field would be given added advantage) |
| CTC | 1.5 lakhs per month (18 lakhs per annum) |
| Experience | <ul style="list-style-type: none"> • At least 3-5 years of full-time experience with minimum of 1-2 years of experience on ELK or similar stack |
| Roles & Responsibilities | <ul style="list-style-type: none"> • Configuration of Kibana visualizations and Elasticsearch instances, or similar tech stack • Creating Log parsers for log ingestion to elastic search through Logstash |

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| | <ul style="list-style-type: none"> • Performing event logs stitching, correlation, and normalization • End-to-end ELK data Ingestion, enrichment, transformation, visualization, and deployment |
| Skills | <ul style="list-style-type: none"> • Expertise in Big data/ data analytics • Logstash pipeline experience, Knowledge of Linux Shell/Python scripting • Experience in Creating Dashboards and visualization on Kibana or similar platforms, index templates and associated index management • Problem-solving with a high level of self-motivation and initiative, and excellent team player spirit |

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| Job Role | C. SOC Analyst - Level 2- Code - T0103 |
| No. of Vacancy | 02 nos. |
| Location | C-DOT Delhi |
| Qualification | Bachelor's degree. (Candidate having graduation/Post graduation in Engineering/Science/Technology in the relevant field would be given added advantage) |
| CTC | 1.5 lakhs per month (18 lakhs per annum for one resource) |
| Experience | <ul style="list-style-type: none"> • At least 5-7 years of full-time experience |
| Roles & Responsibilities | <ul style="list-style-type: none"> • Analysing incidents escalated by the SOC Analyst Level 1 and undertake the detailed investigation of the Security Event • Determine whether the security event will be classified as an incident. • Coordinating with the customer IT and Security team for resolution of the Security Incident • Escalate validated and confirmed incidents to the designated incident response team • Notify the Client of incident and required mitigation works • Fine-tune SIEM rules to reduce false positives and |

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| | <p>remove false negatives</p> <ul style="list-style-type: none"> Proactively research and monitor security information to identify potential threats that may impact the organization |
| Skills | <ul style="list-style-type: none"> Ability to run and understand Sandbox Static Analysis Must have administrative skills in several operating systems, such as Windows, OS X, and Linux Proficiency in basic shell scripting, creating Snort rules, or other log-searching query languages and methods Hands-on experience with Security Information Event Management (SIEM) tools, creating advanced correlation rules, administration of SIEM, system hardening, and Vulnerability Assessments Working experience in firewall and intrusion detection/prevention protocols. Configuration and Troubleshooting experience on Checkpoint, Cisco, Fortigate, PaloAlto and Sonicwall firewalls would be an added advantage Various certifications including Security+, CEH, GIAC, CASP, CISSP is an add-on |

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| Job Role | D. SOC Analyst - Level 1 - Code - T0104 |
| No. of Vacancy | 02 nos. |
| Location | C-DOT Delhi |
| Qualification | Bachelor's degree. (Candidate having graduation/Post graduation in Engineering/Science/Technology in the relevant field would be given added advantage) |
| CTC | 1 lakh per month (12 lakhs per annum for one resource) |
| Experience | <ul style="list-style-type: none"> At least 3-5 years of full-time experience |
| Roles & Responsibilities | <ul style="list-style-type: none"> Handling common security incidents independently, but know when to escalate to higher tiers for support and assistance. |

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| | <ul style="list-style-type: none"> • Providing threat analysis and security logs for security devices • Analysing and responding to hardware and software weaknesses and vulnerabilities. • Providing technical support during incident handling • Investigate, document, and report security problems and emerging security trends • Perform risk analyses, vulnerability testing, and security assessments |
| Skills | <ul style="list-style-type: none"> • Administrative skills in several operating systems, such as Windows, OS X, and Linux • Proficient in basic shell scripting, creating Snort rules, or other log-searching query languages and methods • Work Experience with Security Information and Event Management (SIEM) <p>Sound Technical knowledge on</p> <ul style="list-style-type: none"> • TCP/IP, computer networking, routing and switching • IDS/IPS, penetration and vulnerability testing • Firewall and intrusion detection/prevention protocols • Windows, UNIX and Linux operating systems • Network protocols and packet analysis tools • Anti-virus and anti-malware • Various certifications including Security+, CEH, GIAC, CASP, CISSP is an add-on |

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| Job Role | E. Cloud Administrator - Code - T0105 |
| No. of Vacancy | 02 nos. |
| Location | C-DOT Delhi |
| Qualification | Bachelor's degree. (Candidate having graduation/Post graduation in Engineering/Science/Technology in the relevant field would be given added advantage) |

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| CTC (Consolidated) | 1 lakh per month (12 lakhs per annum for one resource) |
| Experience | <ul style="list-style-type: none"> • At least 3-5 years of full-time experience |
| Roles & Responsibilities | <ul style="list-style-type: none"> • Manage and operate cloud-based hosts (Unix and Windows) • Create and manage the virtualization layer of cloud computing environments (Virtualization) • Operate containerized workloads • Cloud finances –Understand and optimize costs in cloud environments • Support DevOps activities and CI/CD management in the cloud |
| Skills | <ul style="list-style-type: none"> • Hands-on experience in managing Active directory • Ability to create scripts in Python, Perl or other language • Experience in configuration of cloud infrastructure and management solutions and work with the DevOps team to provide cloud support and deployment for existing projects • Strong troubleshooting skills for HTTP response codes, SSO/SAML/SCIM, LDAP, DNS, and email routing/tracing, as well as the ability to maintain security compliance through coordinated Windows and Linux application and system patching • Experience with managing Amazon AWS services and/or Microsoft Azure/ or creation of on-premises cloud infrastructure |

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| Job Role | F. System Administrator - Code - T0106 |
| No. of Vacancy | 02 nos. |
| Location | C-DOT Delhi |
| Qualification | Bachelor's degree. (Candidate having graduation/Post |

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| | graduation in Engineering/Science/Technology in the relevant field would be given added advantage) |
| CTC | 1 lakh per month (12 lakhs per annum for one resource) |
| Experience | <ul style="list-style-type: none"> • At least 3-5 years of full-time experience • Proven experience as a System Administrator, Network Administrator or similar role • Experience with databases, networks (LAN, WAN) and patch management |
| Roles & Responsibilities | <ul style="list-style-type: none"> • Managing Windows, Linux, or Mac systems • Upgrading, installing, and configuring application software and computer hardware • Configuration, Setup, and operationalization of Active Directory • Troubleshooting and providing technical support to employees • Creating and managing system permissions and user accounts • Performing regular security tests and security monitoring • Maintaining networks and network file systems |
| Skills | <ul style="list-style-type: none"> • Proven experience as a System Administrator, Network Administrator or similar role • Experience with databases, networks (LAN, WAN) and patch management • Knowledge of system security (e.g. intrusion detection systems) and data backup/recovery • Ability to create scripts in Python, Perl or other language • Familiarity with various operating systems and platforms • Resourcefulness and problem-solving aptitude |

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| Job Role | G. Senior Lead Developer (C/ C++/ Python)-- Code - T0107 |
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| No. of Vacancy | 05 nos. |
| Location | C-DOT Delhi |
| Qualification | Bachelor's degree. (Candidate having graduation/Post graduation in Engineering/Science/Technology in the relevant field would be given added advantage) |
| CTC | 1.75 lakhs per month (21 lakhs per annum per resource) |
| Experience | <ul style="list-style-type: none"> At least 3-5 years of full-time experience, working as a C/ C++/ Python developer |
| Roles & Responsibilities | <ul style="list-style-type: none"> Design, build, and maintain efficient, reusable, and reliable C/C++/Python code Identify bottlenecks and bugs, and devise solutions to these problems Help maintain code quality, organization, and automatization Knowledge of low-level threading primitives and real-time environments Familiarity with system call wrapper library functions Knowledge of writing native modules for high-level languages such as Node.js, Python, Go, etc. Developing server-side logic, ensuring high performance and responsiveness to requests from the front-end |
| Skills | <ul style="list-style-type: none"> In-depth knowledge of C/C++ language, STL & Multithreading or Python frameworks such as FastAPI, Django, or Flask. Knowledge on AI/ML libraries, such as TensorFlow, PyTorch, and scikit-learn is an add-on. Understanding of database concepts and experience with relational databases (e.g., PostgreSQL, MySQL) or NoSQL databases (e.g., MongoDB) |

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| Job Role | H. Senior React Developer-- Code - T0108 |
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| No. of Vacancy | 02 nos. |
| Location | C-DOT Delhi |
| Qualification | Bachelor's degree. (Candidate having graduation/Post graduation in Engineering/Science/Technology in the relevant field would be given added advantage) |
| CTC | 1.75 lakhs per month (21 lakhs per annum per resource) |
| Experience | <ul style="list-style-type: none"> • At least 3-5 years of full-time experience • Previous experience working as a react.js developer and experience of user interface design |
| Roles & Responsibilities | <ul style="list-style-type: none"> • Skilled react.js developer responsible for developing and implementing user interface components using React.js concepts and workflows such as Redux, Flux, and Webpack • In-depth knowledge of JavaScript and React concepts, excellent front-end coding skills, and a good understanding of progressive web applications • Design and build modern user interface components to enhance application performance |
| Skills | <ul style="list-style-type: none"> • In-depth knowledge of JavaScript, CSS, HTML, and front-end languages • Knowledge of REACT tools including React.js, Webpack, Enzyme, Redux, and Flux • Understanding of database concepts and experience with relational databases (e.g., PostgreSQL, MySQL) or NoSQL databases (e.g., MongoDB) • Excellent troubleshooting skills with browser-based debugging and performance testing software |
| Job Role | I. Senior Network Administrator -- Code - T0109 |
| No. of Vacancy | 01 no. |
| Location | C-DOT Delhi |
| Qualification | Bachelor's degree. (Candidate having graduation/Post |

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| n | graduation in Engineering/Science/Technology in the relevant field would be given added advantage) |
| CTC | 1.5 lakhs per month (18 lakhs per annum for one resource) |
| Experience | <ul style="list-style-type: none"> • At least 3-5 years of full-time experience • Proven experience as a Network Administrator or in a similar role, designing and implementing complex network infrastructure solutions |
| Roles & Responsibilities | <ul style="list-style-type: none"> • Analysing of organizational network architecture and firewall • Able to design and establish a network in an organization • Installing and configuring computer networks and systems • Consulting with clients to specify system requirements and design solutions • Monitoring computer networks and systems to identify how performance can be improved |
| Skills | <ul style="list-style-type: none"> • Strong knowledge of network protocols, architectures, and technologies, including TCP/IP, VLANs, VPNs, MPLS, SD-WAN, DNS, DHCP, and routing protocols (e.g., BGP, OSPF) • Hands-on experience with network hardware, such as routers, switches, firewalls, load balancers, and wireless access points, from vendors like Cisco, Juniper, or Arista • Experience with network security concepts and technologies, including firewalls, VPNs, IDS/IPS, and network access controls |

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| Job Role | Senior Embedded Software Developer - Code - T 0201 |
| No. of Vacancy | 02 no. |
| Location | C-DOT Delhi |
| Qualification | B.E/B.Tech (ECE/CS/EE/EI/ET&T/IT/ICE) or ME/M.Tech (ECE/CS/EE/EI/IT/VLSI/CE/ICE) |
| CTC | Rs 1 lakh to Rs 2 lakh per month consolidated |
| Experience | <ul style="list-style-type: none"> • Experience 4 years+ |
| Roles & Responsibilities | <ul style="list-style-type: none"> • Embedded system requirement gathering and software architecture design • Embedded software module design and implementation in C/ARM assembly in Baremetal & GNU/Linux development environment • Development of HAL and device driver stack for USB/UCPD/SPI/I2C/Ethernet and other peripherals on ARM and STM32 • Software/Hardware co-design and testing • Joint investigation with hardware development team for resolution of peripheral interfaces issues. • Mentoring and guidance to junior engineers • Compilation of feature and module design documents • Maintenance of source code repositories • Provide field site deployment support/site visit |
| Skills | <p>Experience in embedded software design and development, ARM architecture, Low Level C and ARM assembly programming in GNU environment, GNU build environment setup using opensource tools e.g., Yocto, Buildroot etc.,</p> <ul style="list-style-type: none"> • Bootloader and Secure Boot loader configuration, Custom Board bring-up. • Experience in Bare-metal & GNU/Linux embedded software development especially device-driver and system software stack development, good knowledge of RTOS and Linux kernel internals (scheduler, memory management, |

concurrency/synchronization, memory allocation, file systems, networking, storage)

- Good experience in RTOS and Linux device driver development and support

(USB, TCP/IP, Touch Display, Flash storage, I2C, Ethernet PHY-MII/MDIO etc.)

- Open-Source software integration and development.

- Experience in TCP/IP networking stack and USB stack development in Linux/RTOS environment.

- Python and Linux Shell scripting, SCM tools usage experience, e.g., Git

- Experience working with lab tools (Oscilloscope, Trace32 Debuggers, GNU GDB

etc.), Linux and RTOS based application programming experience.

- Follow quality management system activities within the group.

- Ability to work in a team. Excellent communication skills.

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| Job Role | Senior FPGA Software Developer -- Code - T 0202 |
| No. of Vacancy | 04 no. |
| Location | C-DOT Delhi |
| Qualification | B.E/B.Tech (ECE/CS/EE/EI/ET&T/IT/VLSI) or ME/M.Tech (ECE/CS/EE/EI/IT/VLSI/CE) |
| CTC | Rs 1 lakh to Rs 2 lakh per month consolidated |
| Experience | <ul style="list-style-type: none"> • Experience 4 years+ |
| Roles & Responsibilities | <ul style="list-style-type: none"> • Leadership in the design and development of complex FPGA based projects from FPGA selection to architecture, design, implement, synthesize and close timing, simulation, bring-up, debug and verification • Perform detailed design work, including RTL design for FPGAs, component selection, schematic design, and more • Designs, develops, debugs, modifies, and tests designs by using current tools, analysis techniques, and technologies • Work collaboratively and in tandem with other FPGA design engineers and embedded software engineers • Development and maintenance of FPGA-based code to implement algorithms for IP packet capture, crypto data processing and data transmission in an embedded real-time environment utilizing the latest Intel/Xilinx technology and Intel Quartus/Vivado development tools • Designs and implements ARM AXI switches and DMAs between SoC processor and FPGA subsystems. • Work closely with software and hardware teams, providing debug support, technical expertise and relevant documentation • Interface with HW, FW, and SW engineers to test and verify electrical interfaces and protocols between the FPGA and embedded system devices • Verify designs (including SOC architectures utilizing soft-core processors, digital filters, image processing algorithms, and communication interfaces/protocols), |

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| | <p>design and implement test benches and test plans for both chip-level and system level environments</p> <ul style="list-style-type: none"> • Compilation of feature and module design documents • Participate in interdisciplinary design teams and reviews such as peer reviews, code reviews, and other technical exchanges • Maintenance of source code repositories • Provide field site deployment support/site visit |
| <p>Skills</p> | <ul style="list-style-type: none"> • RTL coding and simulation in Verilog, VHDL or System Verilog. • Good coding skills in C, C++, Python • Good knowledge of Xilinx or Intel-Altera FPGA architecture • Exp. with FPGA synthesis tool- Xilinx Vivado, Altera Quartus II or similar, IP Core development, High level synthesis design flow, Logic acceleration & optimization • Experience in creating placement and timing constraints, synthesis, place and route, static timing analysis, DRC/linting analysis, FPGA Timing closure and clock-domain-crossing analysis. • Good knowledge of Linux operating system internals and experience in Linux device driver development. • Knowledge of AMBA-AXI, PCI-E Gen 4, Ethernet, I2C, SPI, MII/MDIO interfaces. • Knowledge of TCP/IP Protocol Suite, Control Protocol such as BGP, IPFIX protocol, DNS, DHCP, HTTP, HTTPS, SIP etc. • Knowledge of IP packet debugging tools such as Wireshark, Tcpdump, ping, traceroute, nmap, hping etc. • Good knowledge of Ethernet/IP packet capture, packet parsing & striping, packet de-duplication, payload compression & decompression, encryption & decryption etc. to be implemented using HDL for FPGA • Experience mapping algorithms and standards (PCIe, NVMe, USB, Ethernet, TCP/IP, TCP/IP off load engine (TOE), SERDES, LVDS, and Memory Controllers – DDR4) to hardware and architecture/system design tradeoffs. • Experience with High-speed network interface (SFP/QSFP) design in FPGA • Selected candidates will be responsible to work on FPGA hardware and development tools to accelerate various functions of an application using FPGA offloading. • Exposure to OpenCL and high-level synthesis on FPGA |

platforms is desirable

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| Job Role | Embedded Platform and Device Driver Software Developer -- Code - T 0203 |
| No. of Vacancy | 02 no. |
| Location | C-DOT Delhi |
| Qualification | B.E/B.Tech (ECE/CS/EE/EI/IT/ICE) or ME/M.Tech (ECE/CS/EE/EI/IT/CE/ICE) |
| CTC | Rs 1 lakh to Rs 2 lakh per month consolidated |
| Experience | <ul style="list-style-type: none"> • Experience 2 years+ |
| Roles & Responsibilities | <ul style="list-style-type: none"> • Embedded system requirement gathering and software architecture design • Embedded platform software module & device driver design and implementation in C/C++ within GNU/Linux development environment • Debugging & development of features within device driver for TPM, Touch display, Camera, Audiodcodec, Wifi, Bluetooth and other peripherals on ARM • Working as an Individual Contributor in the field of Platform Software • Joint investigation with hardware development team for resolution of peripheral interfaces issues. • Mentoring and guidance to junior engineers • Documentation of all-related documents as per CMMI SDLC • Maintenance of source code repositories • Provide field site deployment support / site visit |
| Skills | <p>Experience in embedded software design and development, ARM architecture, Low Level C and ARM assembly programming in GNU environment, GNU build environment setup using opensource tools e.g., Yocto, Buildroot etc.,</p> <ul style="list-style-type: none"> • Bootloader and Secure Boot loader configuration, Custom Board bring-up • Experience in embedded software development especially device-driver and system software stack development, good knowledge of Linux kernel internals |

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| | <ul style="list-style-type: none">• Hands-on experience in Linux device driver development and support (TPM, Touch Display, Camera, Audio, I2C, Wifi, Bluetooth, Ethernet PHY-MII/MDIO, UART, GPIO etc.)• Experience of debugging platform software & device driver using JTAG debugger• Knowledge of device tree programming, secure boot and security related features in Platform software• Open-Source software integration and development.• Knowledge of VOIP, SIP, Audio/Video Codecs, etc. is desirable• Experience in Python and Linux Shell scripting, SCM tools usage experience, e.g. Git• Experience working with lab tools (Trace32 Debuggers, GNU GDB etc.), Linux-based application programming experience• Follow quality management system activities within the group.• Ability to work in a team. Excellent communication & documentation skills.• Capable to support field site deployment support/site visit |
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